

DEC 08 2010

Application No. 10/583,397
Reply to Office Action dated 09/08/2010

Amendments to the Specification:

Please amend the paragraph bridging pages 10 and 11 as follows:

The bus interface multiplexing request module 710 of the i960-like interface judges whether write buffer enables, if the write buffer does not enable, then sends bus request signal REQUEST_BIU to the bus interface multiplexing controller (Bus Interface Unit) upon determining there is accessing instruction (including read and write operation instructions) from AMBA_AHB bus, then judge and detect the control signal HREADY_IN from AMBA AHB bus, if it is detected that HREADY_IN is in logic high level, then stop sending bus request signal REQUEST_BIU to the bus interface multiplexing controller BIU; if the write buffer enables, then send bus request signal REQUEST_BIU to bus interface multiplexing controller BIU when determine that the state indicating signal AD_GEN_state from the main controller indicates read or write, then judge and detect the RDYRCV_reg signal from the i960-like bus, if it is detected that signal RDYRCV_reg is in logic low level, then stop sending bus request signal REQUEST_BIU to the bus interface multiplexing controller BIU. If the present embodiment does not multiplex with other similar bus interface controller, but directly connects to the i960-like bus, then the input signal GRANT_BIU which is from the bus interface multiplexing controller BIU should be set to be valid constantly, i.e. in logic high level, so that the converter's operation will not be affected by the bus request signal REQUEST_BIU from this module; in addition, it is able to take the bus request signal REQUEST_BIU from this module as control signal according to the initial configuration information from the AHB bus, so as to ~~dynamatically~~ dynamically control the i960-like bus clock output from this interface converter; when there is communication from AHB to i960-like, the i960-like bus clock is valid, when there is no communication, then automatically set the i960-like bus clock high and suspend, so as to save the power of the chip or module connected to the present embodiment via the i960-like bus.